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Kind regards,

Team Nexperia

# 74HC00; 74HCT00

## Quad 2-input NAND gate

Rev. 7 — 25 November 2015

Product data sheet

### 1. General description

The 74HC00; 74HCT00 is a quad 2-input NAND gate. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of  $V_{CC}$ .

### 2. Features and benefits

- Input levels:
  - ◆ For 74HC00: CMOS level
  - ◆ For 74HCT00: TTL level
- Complies with JEDEC standard no. 7A
- ESD protection:
  - ◆ HBM JESD22-A114F exceeds 2000 V
  - ◆ MM JESD22-A115-A exceeds 200 V
- Multiple package options
- Specified from  $-40\text{ }^{\circ}\text{C}$  to  $+85\text{ }^{\circ}\text{C}$  and from  $-40\text{ }^{\circ}\text{C}$  to  $+125\text{ }^{\circ}\text{C}$

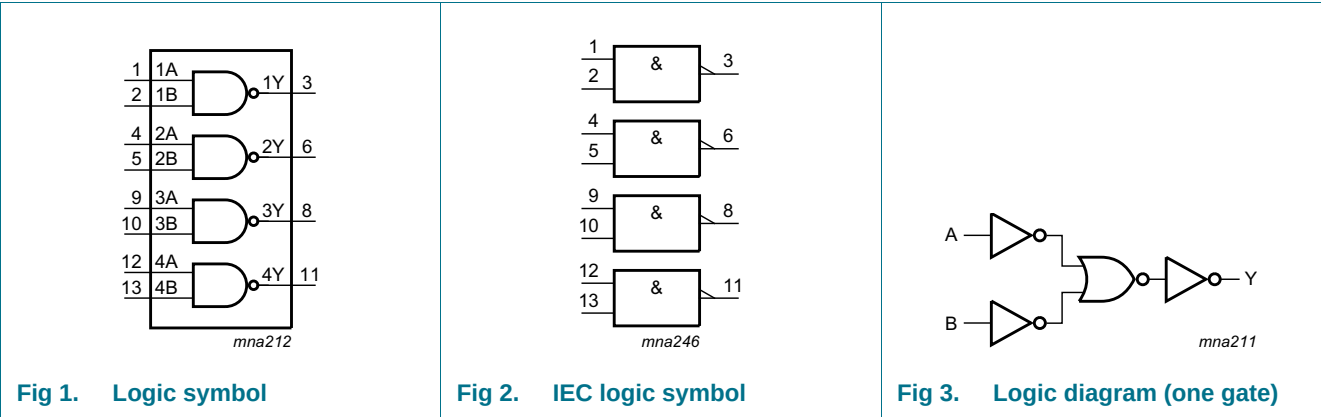
### 3. Ordering information

Table 1. Ordering information

| Type number | Package                                                         |          |                                                                                                                                                 |          |
|-------------|-----------------------------------------------------------------|----------|-------------------------------------------------------------------------------------------------------------------------------------------------|----------|
|             | Temperature range                                               | Name     | Description                                                                                                                                     | Version  |
| 74HC00D     | $-40\text{ }^{\circ}\text{C}$ to $+125\text{ }^{\circ}\text{C}$ | SO14     | plastic small outline package; 14 leads; body width 3.9 mm                                                                                      | SOT108-1 |
| 74HCT00D    |                                                                 |          |                                                                                                                                                 |          |
| 74HC00DB    | $-40\text{ }^{\circ}\text{C}$ to $+125\text{ }^{\circ}\text{C}$ | SSOP14   | plastic shrink small outline package; 14 leads; body width 5.3 mm                                                                               | SOT337-1 |
| 74HCT00DB   |                                                                 |          |                                                                                                                                                 |          |
| 74HC00PW    | $-40\text{ }^{\circ}\text{C}$ to $+125\text{ }^{\circ}\text{C}$ | TSSOP14  | plastic thin shrink small outline package; 14 leads; body width 4.4 mm                                                                          | SOT402-1 |
| 74HCT00PW   |                                                                 |          |                                                                                                                                                 |          |
| 74HC00BQ    | $-40\text{ }^{\circ}\text{C}$ to $+125\text{ }^{\circ}\text{C}$ | DHVQFN14 | plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads; 14 terminals; body $2.5 \times 3 \times 0.85\text{ mm}$ | SOT762-1 |
| 74HCT00BQ   |                                                                 |          |                                                                                                                                                 |          |

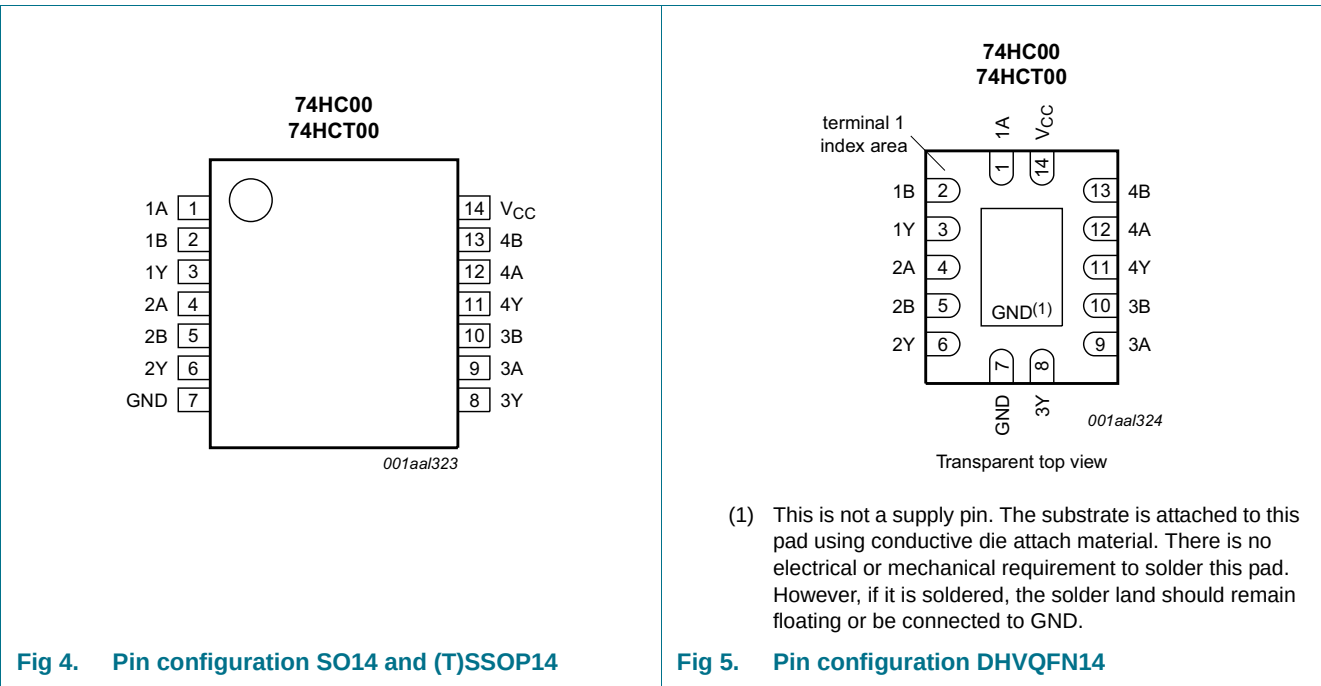


4. Functional diagram



5. Pinning information

5.1 Pinning



## 5.2 Pin description

Table 2. Pin description

| Symbol          | Pin          | Description    |
|-----------------|--------------|----------------|
| 1A to 4A        | 1, 4, 9, 12  | data input     |
| 1B to 4B        | 2, 5, 10, 13 | data input     |
| 1Y to 4Y        | 3, 6, 8, 11  | data output    |
| GND             | 7            | ground (0 V)   |
| V <sub>CC</sub> | 14           | supply voltage |

## 6. Functional description

Table 3. Function table<sup>[1]</sup>

| Input |    | Output |
|-------|----|--------|
| nA    | nB | nY     |
| L     | X  | H      |
| X     | L  | H      |
| H     | H  | L      |

[1] H = HIGH voltage level; L = LOW voltage level; X = don't care.

## 7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

| Symbol           | Parameter                              | Conditions                                                                         | Min  | Max  | Unit |
|------------------|----------------------------------------|------------------------------------------------------------------------------------|------|------|------|
| V <sub>CC</sub>  | supply voltage                         |                                                                                    | -0.5 | +7   | V    |
| I <sub>IK</sub>  | input clamping current                 | V <sub>I</sub> < -0.5 V or V <sub>I</sub> > V <sub>CC</sub> + 0.5 V <sup>[1]</sup> | -    | ±20  | mA   |
| I <sub>OK</sub>  | output clamping current                | V <sub>O</sub> < -0.5 V or V <sub>O</sub> > V <sub>CC</sub> + 0.5 V <sup>[1]</sup> | -    | ±20  | mA   |
| I <sub>O</sub>   | output current                         | -0.5 V < V <sub>O</sub> < V <sub>CC</sub> + 0.5 V                                  | -    | ±25  | mA   |
| I <sub>CC</sub>  | supply current                         |                                                                                    | -    | 50   | mA   |
| I <sub>GND</sub> | ground current                         |                                                                                    | -50  | -    | mA   |
| T <sub>stg</sub> | storage temperature                    |                                                                                    | -65  | +150 | °C   |
| P <sub>tot</sub> | total power dissipation <sup>[2]</sup> |                                                                                    |      |      |      |
|                  | SO14, (T)SSOP14 and DHVQFN14 packages  |                                                                                    | -    | 500  | mW   |

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SO14 package: P<sub>tot</sub> derates linearly with 8 mW/K above 70 °C.

For (T)SSOP14 packages: P<sub>tot</sub> derates linearly with 5.5 mW/K above 60 °C.

For DHVQFN14 packages: P<sub>tot</sub> derates linearly with 4.5 mW/K above 60 °C.

## 8. Recommended operating conditions

**Table 5. Recommended operating conditions**

Voltages are referenced to GND (ground = 0 V)

| Symbol           | Parameter                           | Conditions              | 74HC00 |      |                 | 74HCT00 |      |                 | Unit |
|------------------|-------------------------------------|-------------------------|--------|------|-----------------|---------|------|-----------------|------|
|                  |                                     |                         | Min    | Typ  | Max             | Min     | Typ  | Max             |      |
| V <sub>CC</sub>  | supply voltage                      |                         | 2.0    | 5.0  | 6.0             | 4.5     | 5.0  | 5.5             | V    |
| V <sub>I</sub>   | input voltage                       |                         | 0      | -    | V <sub>CC</sub> | 0       | -    | V <sub>CC</sub> | V    |
| V <sub>O</sub>   | output voltage                      |                         | 0      | -    | V <sub>CC</sub> | 0       | -    | V <sub>CC</sub> | V    |
| T <sub>amb</sub> | ambient temperature                 |                         | -40    | +25  | +125            | -40     | +25  | +125            | °C   |
| Δt/ΔV            | input transition rise and fall rate | V <sub>CC</sub> = 2.0 V | -      | -    | 625             | -       | -    | -               | ns/V |
|                  |                                     | V <sub>CC</sub> = 4.5 V | -      | 1.67 | 139             | -       | 1.67 | 139             | ns/V |
|                  |                                     | V <sub>CC</sub> = 6.0 V | -      | -    | 83              | -       | -    | -               | ns/V |

## 9. Static characteristics

**Table 6. Static characteristics**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

| Symbol          | Parameter                 | Conditions                                                                             | 25 °C |      |     | −40 °C to +85 °C |      | −40 °C to +125 °C |      | Unit |
|-----------------|---------------------------|----------------------------------------------------------------------------------------|-------|------|-----|------------------|------|-------------------|------|------|
|                 |                           |                                                                                        | Min   | Typ  | Max | Min              | Max  | Min               | Max  |      |
| 74HC00          |                           |                                                                                        |       |      |     |                  |      |                   |      |      |
| V <sub>IH</sub> | HIGH-level input voltage  | V <sub>CC</sub> = 2.0 V                                                                | -     | 1.2  | -   | 1.5              | -    | 1.5               | -    | V    |
|                 |                           | V <sub>CC</sub> = 4.5 V                                                                | -     | 2.4  | -   | 3.15             | -    | 3.15              | -    | V    |
|                 |                           | V <sub>CC</sub> = 6.0 V                                                                | -     | 3.2  | -   | 4.2              | -    | 4.2               | -    | V    |
| V <sub>IL</sub> | LOW-level input voltage   | V <sub>CC</sub> = 2.0 V                                                                | -     | 0.8  | -   | -                | 0.5  | -                 | 0.5  | V    |
|                 |                           | V <sub>CC</sub> = 4.5 V                                                                | -     | 2.1  | -   | -                | 1.35 | -                 | 1.35 | V    |
|                 |                           | V <sub>CC</sub> = 6.0 V                                                                | -     | 2.8  | -   | -                | 1.8  | -                 | 1.8  | V    |
| V <sub>OH</sub> | HIGH-level output voltage | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>                                    |       |      |     |                  |      |                   |      |      |
|                 |                           | I <sub>O</sub> = −20 μA; V <sub>CC</sub> = 2.0 V                                       | -     | 2.0  | -   | 1.9              | -    | 1.9               | -    | V    |
|                 |                           | I <sub>O</sub> = −20 μA; V <sub>CC</sub> = 4.5 V                                       | -     | 4.5  | -   | 4.4              | -    | 4.4               | -    | V    |
|                 |                           | I <sub>O</sub> = −20 μA; V <sub>CC</sub> = 6.0 V                                       | -     | 6.0  | -   | 5.9              | -    | 5.9               | -    | V    |
|                 |                           | I <sub>O</sub> = −4.0 mA; V <sub>CC</sub> = 4.5 V                                      | -     | 4.32 | -   | 3.84             | -    | 3.7               | -    | V    |
|                 |                           | I <sub>O</sub> = −5.2 mA; V <sub>CC</sub> = 6.0 V                                      | -     | 5.81 | -   | 5.34             | -    | 5.2               | -    | V    |
| V <sub>OL</sub> | LOW-level output voltage  | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>                                    |       |      |     |                  |      |                   |      |      |
|                 |                           | I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 2.0 V                                        | -     | 0    | -   | -                | 0.1  | -                 | 0.1  | V    |
|                 |                           | I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 4.5 V                                        | -     | 0    | -   | -                | 0.1  | -                 | 0.1  | V    |
|                 |                           | I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 6.0 V                                        | -     | 0    | -   | -                | 0.1  | -                 | 0.1  | V    |
|                 |                           | I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 4.5 V                                       | -     | 0.15 | -   | -                | 0.33 | -                 | 0.4  | V    |
|                 |                           | I <sub>O</sub> = 5.2 mA; V <sub>CC</sub> = 6.0 V                                       | -     | 0.16 | -   | -                | 0.33 | -                 | 0.4  | V    |
| I <sub>I</sub>  | input leakage current     | V <sub>I</sub> = V <sub>CC</sub> or GND; V <sub>CC</sub> = 6.0 V                       | -     | -    | -   | -                | ±1   | -                 | ±1   | μA   |
| I <sub>CC</sub> | supply current            | V <sub>I</sub> = V <sub>CC</sub> or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 6.0 V | -     | -    | -   | -                | 20   | -                 | 40   | μA   |

**Table 6.** Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

| Symbol           | Parameter                 | Conditions                                                                                                                                                       | 25 °C |      |     | –40 °C to +85 °C |      | –40 °C to +125 °C |     | Unit |
|------------------|---------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------|------|-----|------------------|------|-------------------|-----|------|
|                  |                           |                                                                                                                                                                  | Min   | Typ  | Max | Min              | Max  | Min               | Max |      |
| C <sub>I</sub>   | input capacitance         |                                                                                                                                                                  | -     | 3.5  | -   | -                | -    | -                 | -   | pF   |
| <b>74HCT00</b>   |                           |                                                                                                                                                                  |       |      |     |                  |      |                   |     |      |
| V <sub>IH</sub>  | HIGH-level input voltage  | V <sub>CC</sub> = 4.5 V to 5.5 V                                                                                                                                 | -     | 1.6  | -   | 2.0              | -    | 2.0               | -   | V    |
| V <sub>IL</sub>  | LOW-level input voltage   | V <sub>CC</sub> = 4.5 V to 5.5 V                                                                                                                                 | -     | 1.2  | -   | -                | 0.8  | -                 | 0.8 | V    |
| V <sub>OH</sub>  | HIGH-level output voltage | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> ; V <sub>CC</sub> = 4.5 V                                                                                    |       |      |     |                  |      |                   |     |      |
|                  |                           | I <sub>O</sub> = –20 µA                                                                                                                                          | -     | 4.5  | -   | 4.4              | -    | 4.4               | -   | V    |
|                  |                           | I <sub>O</sub> = –4.0 mA                                                                                                                                         | -     | 4.32 | -   | 3.84             | -    | 3.7               | -   | V    |
| V <sub>OL</sub>  | LOW-level output voltage  | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> ; V <sub>CC</sub> = 4.5 V                                                                                    |       |      |     |                  |      |                   |     |      |
|                  |                           | I <sub>O</sub> = 20 µA; V <sub>CC</sub> = 4.5 V                                                                                                                  | -     | 0    | -   | -                | 0.1  | -                 | 0.1 | V    |
|                  |                           | I <sub>O</sub> = 5.2 mA; V <sub>CC</sub> = 6.0 V                                                                                                                 | -     | 0.15 | -   | -                | 0.33 | -                 | 0.4 | V    |
| I <sub>I</sub>   | input leakage current     | V <sub>I</sub> = V <sub>CC</sub> or GND;<br>V <sub>CC</sub> = 6.0 V                                                                                              | -     | -    | -   | -                | ±1   | -                 | ±1  | µA   |
| I <sub>CC</sub>  | supply current            | V <sub>I</sub> = V <sub>CC</sub> or GND; I <sub>O</sub> = 0 A;<br>V <sub>CC</sub> = 6.0 V                                                                        | -     | -    | -   | -                | 20   | -                 | 40  | µA   |
| ΔI <sub>CC</sub> | additional supply current | per input pin;<br>V <sub>I</sub> = V <sub>CC</sub> – 2.1 V; I <sub>O</sub> = 0 A;<br>other inputs at V <sub>CC</sub> or GND;<br>V <sub>CC</sub> = 4.5 V to 5.5 V | -     | 150  | -   | -                | 675  | -                 | 735 | µA   |
| C <sub>I</sub>   | input capacitance         |                                                                                                                                                                  | -     | 3.5  | -   | -                | -    | -                 | -   | pF   |

## 10. Dynamic characteristics

**Table 7.** Dynamic characteristicsGND = 0 V; C<sub>L</sub> = 50 pF; for test circuit see [Figure 7](#).

| Symbol          | Parameter         | Conditions                                                     | 25 °C |     |     | –40 °C to +125 °C |              | Unit |
|-----------------|-------------------|----------------------------------------------------------------|-------|-----|-----|-------------------|--------------|------|
|                 |                   |                                                                | Min   | Typ | Max | Max (85 °C)       | Max (125 °C) |      |
| 74HC00          |                   |                                                                |       |     |     |                   |              |      |
| t <sub>pd</sub> | propagation delay | nA, nB to nY; see <a href="#">Figure 6</a> <a href="#">[1]</a> |       |     |     |                   |              |      |
|                 |                   | V <sub>CC</sub> = 2.0 V                                        | -     | 25  | -   | 115               | 135          | ns   |
|                 |                   | V <sub>CC</sub> = 4.5 V                                        | -     | 9   | -   | 23                | 27           | ns   |
|                 |                   | V <sub>CC</sub> = 5.0 V; C <sub>L</sub> = 15 pF                | -     | 7   | -   | -                 | -            | ns   |
|                 |                   | V <sub>CC</sub> = 6.0 V                                        | -     | 7   | -   | 20                | 23           | ns   |
| t <sub>t</sub>  | transition time   | see <a href="#">Figure 6</a> <a href="#">[2]</a>               |       |     |     |                   |              |      |
|                 |                   | V <sub>CC</sub> = 2.0 V                                        | -     | 19  | -   | 95                | 110          | ns   |
|                 |                   | V <sub>CC</sub> = 4.5 V                                        | -     | 7   | -   | 19                | 22           | ns   |
|                 |                   | V <sub>CC</sub> = 6.0 V                                        | -     | 6   | -   | 16                | 19           | ns   |

**Table 7. Dynamic characteristics ...continued**  
 $GND = 0\text{ V}$ ;  $C_L = 50\text{ pF}$ ; for test circuit see [Figure 7](#).

| Symbol         | Parameter                     | Conditions                                                                 | 25 °C |     |     | –40 °C to +125 °C |              | Unit |
|----------------|-------------------------------|----------------------------------------------------------------------------|-------|-----|-----|-------------------|--------------|------|
|                |                               |                                                                            | Min   | Typ | Max | Max (85 °C)       | Max (125 °C) |      |
| $C_{PD}$       | power dissipation capacitance | per package; $V_I = GND$ to $V_{CC}$ <a href="#">[3]</a>                   | -     | 22  | -   | -                 | -            | pF   |
| <b>74HCT00</b> |                               |                                                                            |       |     |     |                   |              |      |
| $t_{pd}$       | propagation delay             | nA, nB to nY; see <a href="#">Figure 6</a> <a href="#">[1]</a>             |       |     |     |                   |              |      |
|                |                               | $V_{CC} = 4.5\text{ V}$                                                    | -     | 12  | -   | 24                | 29           | ns   |
|                |                               | $V_{CC} = 5.0\text{ V}$ ; $C_L = 15\text{ pF}$                             | -     | 10  | -   | -                 | -            | ns   |
| $t_t$          | transition time               | $V_{CC} = 4.5\text{ V}$ ; see <a href="#">Figure 6</a> <a href="#">[2]</a> | -     | -   | -   | 29                | 22           | ns   |
| $C_{PD}$       | power dissipation capacitance | per package; $V_I = GND$ to $V_{CC} - 1.5\text{ V}$ <a href="#">[3]</a>    | -     | 22  | -   | -                 | -            | pF   |

[1]  $t_{pd}$  is the same as  $t_{PHL}$  and  $t_{PLH}$ .

[2]  $t_t$  is the same as  $t_{THL}$  and  $t_{TLH}$ .

[3]  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu\text{W}$ ):

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum (C_L \times V_{CC}^2 \times f_o) \text{ where:}$$

$f_i$  = input frequency in MHz;

$f_o$  = output frequency in MHz;

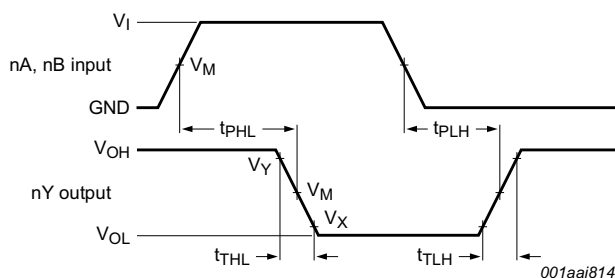
$C_L$  = output load capacitance in pF;

$V_{CC}$  = supply voltage in V;

$N$  = number of inputs switching;

$\sum (C_L \times V_{CC}^2 \times f_o)$  = sum of outputs.

## 11. Waveforms



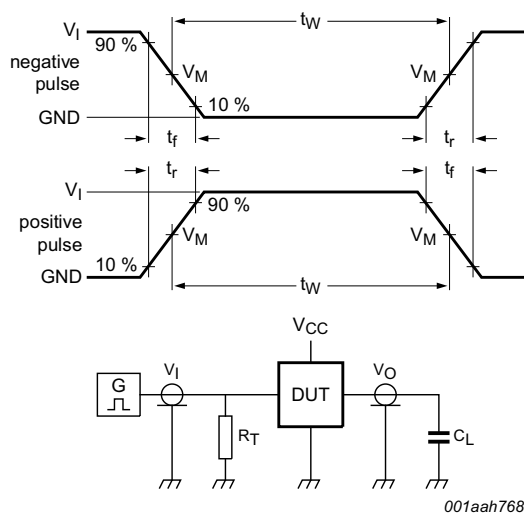
Measurement points are given in [Table 9](#).

$V_{OL}$  and  $V_{OH}$  are typical voltage output levels that occur with the output load.

**Fig 6. Input to output propagation delays**

**Table 8. Measurement points**

| Type    | Input       | Output      |             |             |
|---------|-------------|-------------|-------------|-------------|
|         | $V_M$       | $V_M$       | $V_X$       | $V_Y$       |
| 74HC00  | $0.5V_{CC}$ | $0.5V_{CC}$ | $0.1V_{CC}$ | $0.9V_{CC}$ |
| 74HCT00 | 1.3 V       | 1.3 V       | $0.1V_{CC}$ | $0.9V_{CC}$ |



Test data is given in Table 9.

Definitions test circuit:

$R_T$  = termination resistance should be equal to output impedance  $Z_0$  of the pulse generator.

$C_L$  = load capacitance including jig and probe capacitance.

**Fig 7. Test circuit for measuring switching times**

### Table 9. Test data

| Type    | Input           |                                 | Load           | Test                                |
|---------|-----------------|---------------------------------|----------------|-------------------------------------|
|         | V <sub>I</sub>  | t <sub>r</sub> , t <sub>f</sub> | C <sub>L</sub> |                                     |
| 74HC00  | V <sub>CC</sub> | 6.0 ns                          | 15 pF, 50 pF   | t <sub>PLH</sub> , t <sub>PHL</sub> |
| 74HCT00 | 3.0 V           | 6.0 ns                          | 15 pF, 50 pF   | t <sub>PLH</sub> , t <sub>PHL</sub> |



12. Package outline

SO14: plastic small outline package; 14 leads; body width 3.9 mm

SOT108-1

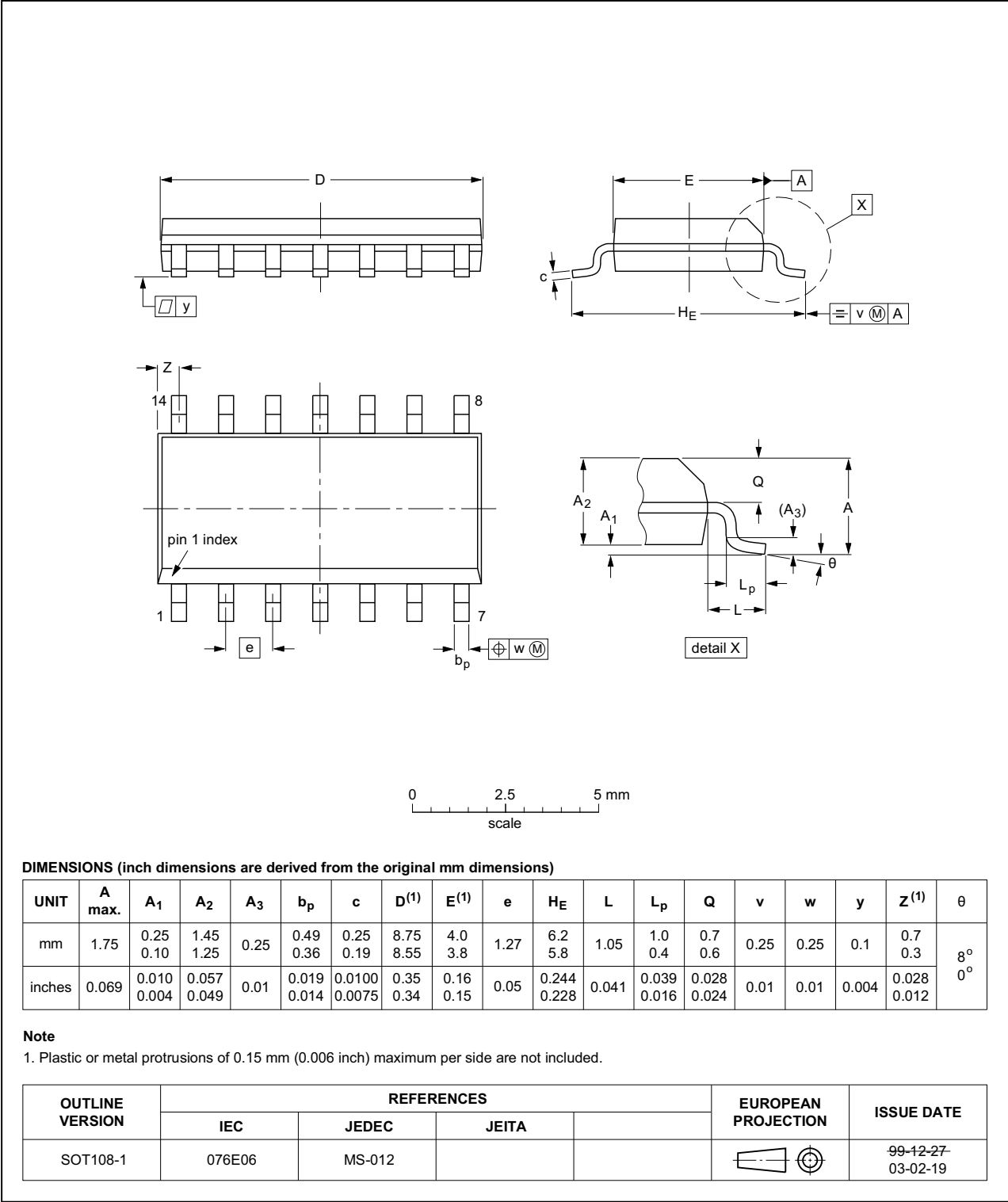
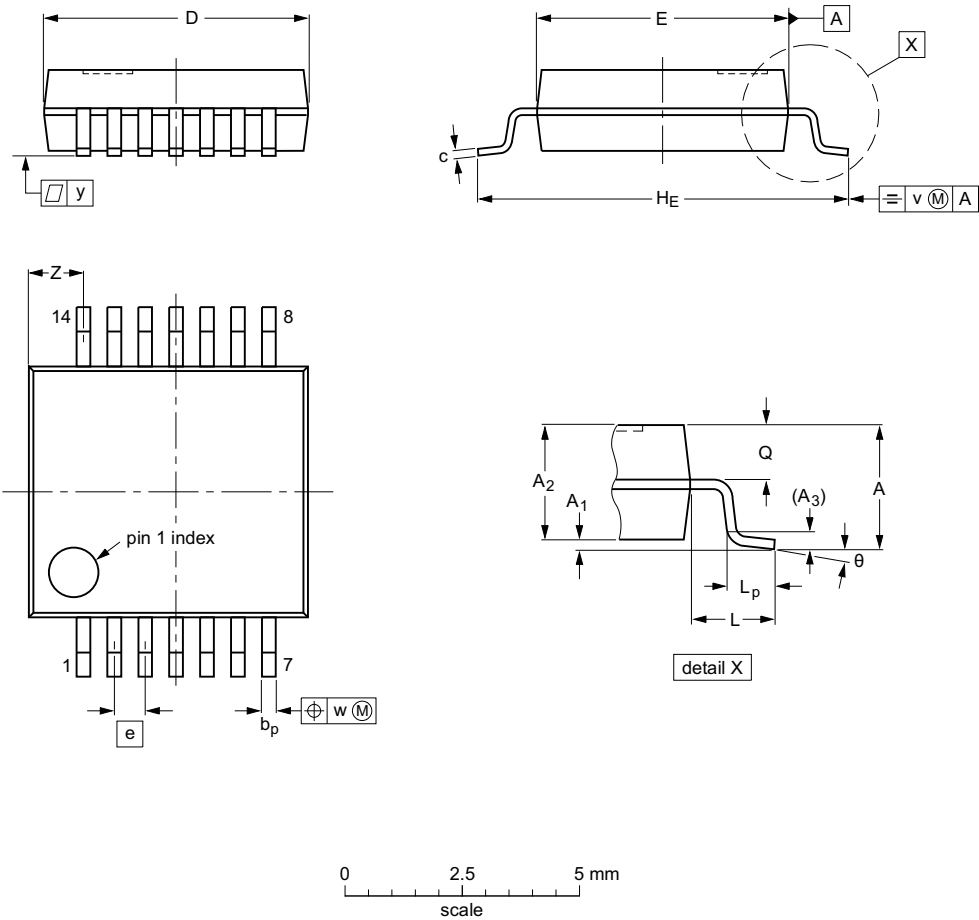


Fig 8. Package outline SOT108-1 (SO14)

SSOP14: plastic shrink small outline package; 14 leads; body width 5.3 mm

SOT337-1



DIMENSIONS (mm are the original dimensions)

| UNIT | A<br>max. | A <sub>1</sub> | A <sub>2</sub> | A <sub>3</sub> | b <sub>p</sub> | c            | D <sup>(1)</sup> | E <sup>(1)</sup> | e    | H <sub>E</sub> | L    | L <sub>p</sub> | Q          | v   | w    | y   | Z <sup>(1)</sup> | θ        |
|------|-----------|----------------|----------------|----------------|----------------|--------------|------------------|------------------|------|----------------|------|----------------|------------|-----|------|-----|------------------|----------|
| mm   | 2         | 0.21<br>0.05   | 1.80<br>1.65   | 0.25           | 0.38<br>0.25   | 0.20<br>0.09 | 6.4<br>6.0       | 5.4<br>5.2       | 0.65 | 7.9<br>7.6     | 1.25 | 1.03<br>0.63   | 0.9<br>0.7 | 0.2 | 0.13 | 0.1 | 1.4<br>0.9       | 8°<br>0° |

**Note**  
 1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

| OUTLINE<br>VERSION | REFERENCES |        |       |  | EUROPEAN<br>PROJECTION | ISSUE DATE            |
|--------------------|------------|--------|-------|--|------------------------|-----------------------|
|                    | IEC        | JEDEC  | JEITA |  |                        |                       |
| SOT337-1           |            | MO-150 |       |  |                        | -99-12-27<br>03-02-19 |

Fig 9. Package outline SOT337-1 (SSOP14)

TSSOP14: plastic thin shrink small outline package; 14 leads; body width 4.4 mm

SOT402-1

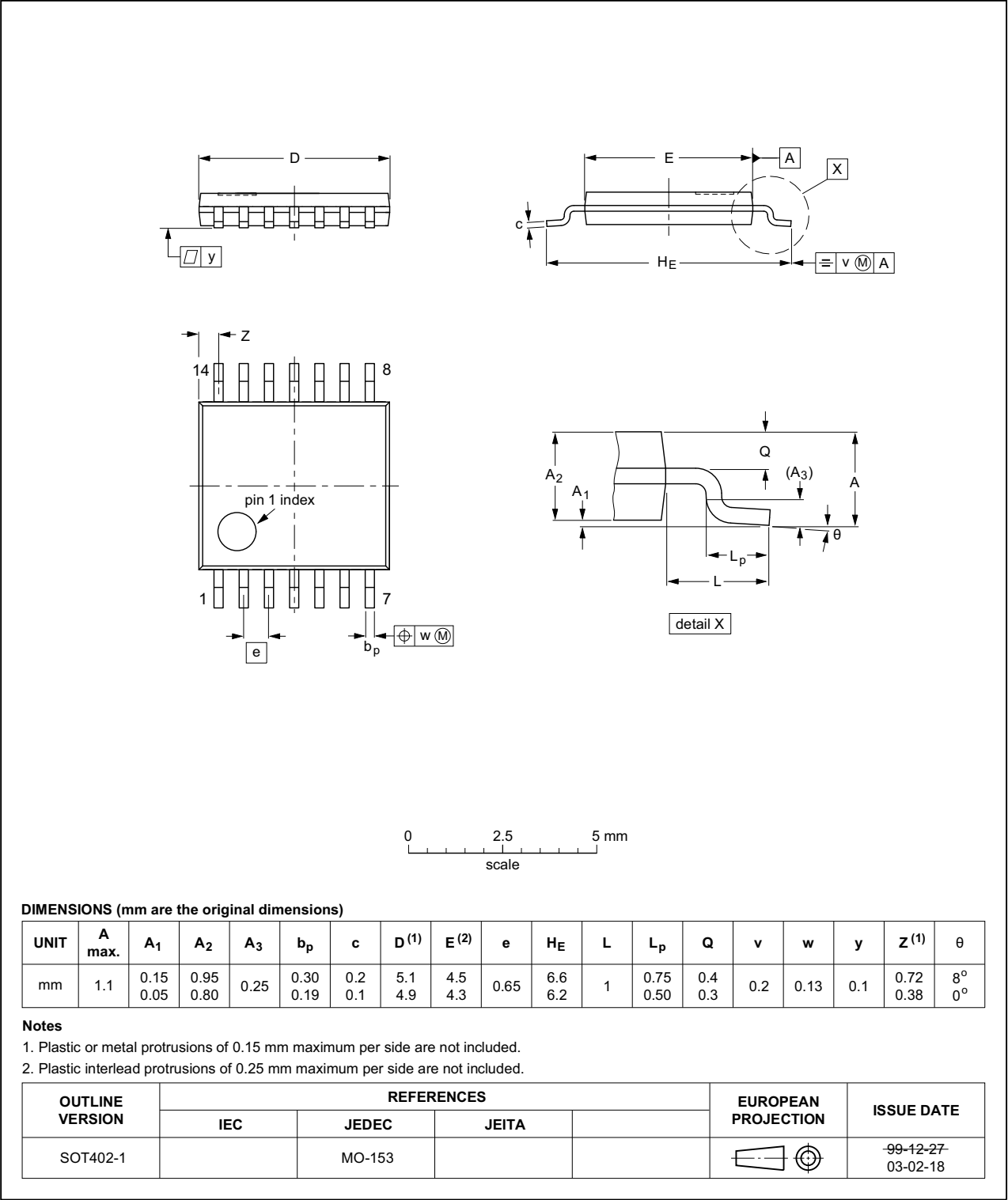


Fig 10. Package outline SOT402-1 (TSSOP14)

DHVQFN14: plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads;  
14 terminals; body 2.5 x 3 x 0.85 mm

SOT762-1

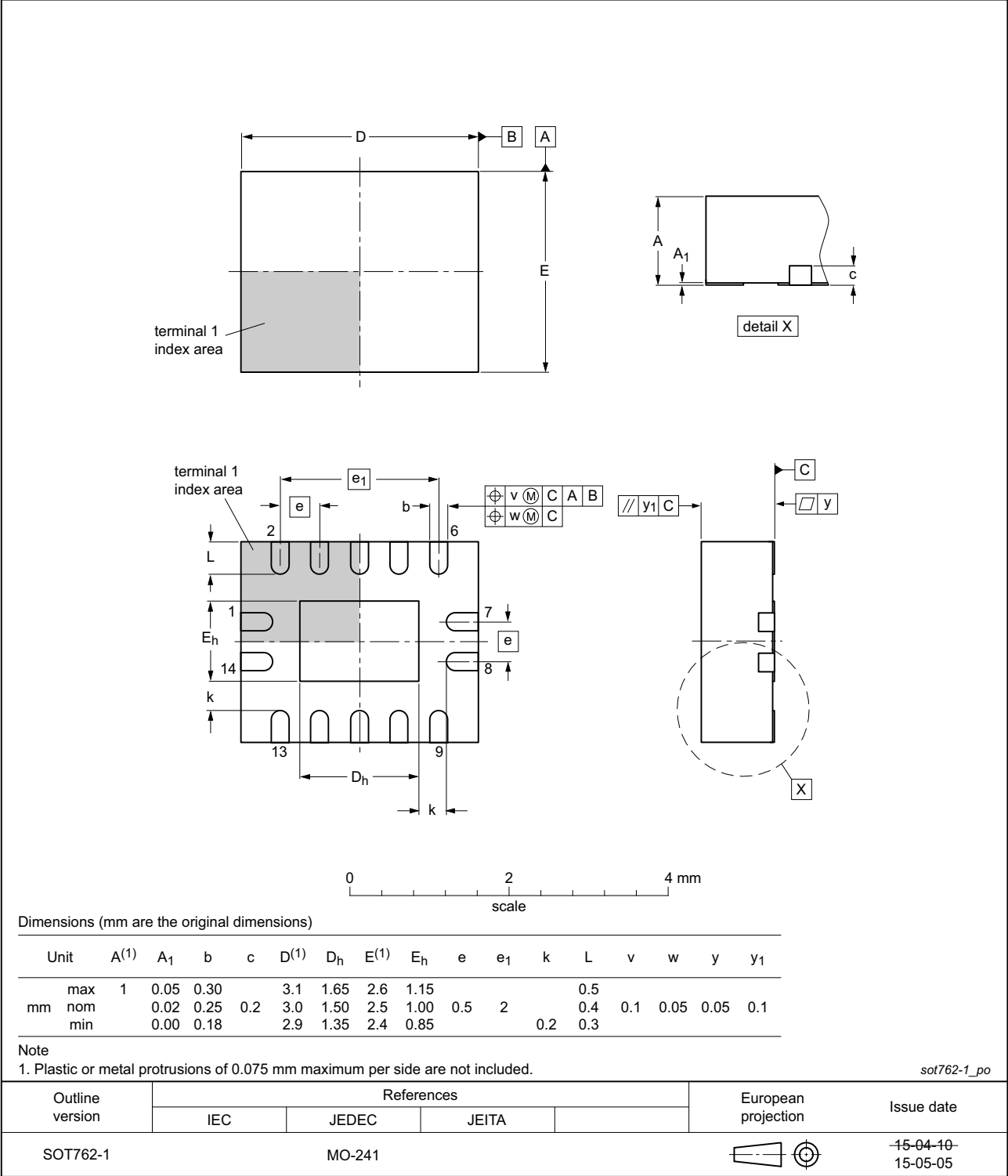


Fig 11. Package outline SOT762-1 (DHVQFN14)

## 13. Abbreviations

Table 10. Abbreviations

| Acronym | Description                                    |
|---------|------------------------------------------------|
| CMOS    | Complementary Metal-Oxide Semiconductor        |
| DUT     | Device Under Test                              |
| ESD     | ElectroStatic Discharge                        |
| HBM     | Human Body Model                               |
| LSTTL   | Low-power Schottky Transistor-Transistor Logic |
| MM      | Machine Model                                  |
| TTL     | Transistor-Transistor Logic                    |

## 14. Revision history

Table 11. Revision history

| Document ID        | Release date                                           | Data sheet status     | Change notice | Supersedes         |
|--------------------|--------------------------------------------------------|-----------------------|---------------|--------------------|
| 74HC_HCT00 v.7     | 20151125                                               | Product data sheet    | -             | 74HC_HCT00 v.6     |
| Modifications:     | • Type numbers 74HC00N and 74HCT00N (SOT27-1) removed. |                       |               |                    |
| 74HC_HCT00 v.6     | 20111214                                               | Product data sheet    | -             | 74HC_HCT00 v.5     |
| Modifications:     | • Legal pages updated.                                 |                       |               |                    |
| 74HC_HCT00 v.5     | 20101125                                               | Product data sheet    | -             | 74HC_HCT00 v.4     |
| 74HC_HCT00 v.4     | 20100111                                               | Product data sheet    | -             | 74HC_HCT00 v.3     |
| 74HC_HCT00 v.3     | 20030630                                               | Product data sheet    | -             | 74HC_HCT00_CNV v.2 |
| 74HC_HCT00_CNV v.2 | 19970826                                               | Product specification | -             | -                  |

## 15. Legal information

### 15.1 Data sheet status

| Document status <sup>[1][2]</sup> | Product status <sup>[3]</sup> | Definition                                                                            |
|-----------------------------------|-------------------------------|---------------------------------------------------------------------------------------|
| Objective [short] data sheet      | Development                   | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet    | Qualification                 | This document contains data from the preliminary specification.                       |
| Product [short] data sheet        | Production                    | This document contains the product specification.                                     |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

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